***Archith Venukumar***  *103, Ramya Regency,  
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*Email: archith.venu@gmail.com*

Mar 29th, 2013

Rochester Institute of Technology

Attention: Office of Student Affairs

Sir/Madam,

**Ref: ID Number: 845005081**

I would like to begin by saying it has been a dream come true to be accepted into Rochester Institute of Technology. The university has long been known for its experiential method of learning and co-op program in addition to imparting quality education. Having been given the opportunity to apply for the position of Graduate Assistant at various departments at the university, I intend to make full use of my talents and qualifications to perform the role assigned to me to the best of my ability. In addition to the details mentioned in the required essays, I believe that my hands on experience with Office Suite and love for words and numbers combined with my adaptability and outgoing nature will go a long way in helping me performing my duties as Graduate assistant. I look forward to hearing positive news from the department and hope to sustain a long and fulfilling career at the university.

Thanking you,

Archith Venukumar

Archith Venukumar

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| **Email:** [archith.venu@gmail.com](mailto:archith.venu@gmail.com) | **Tel:**+919980533059 |

**Objective**

*To work smart and contribute effectively to my office/department, gain valuable experience and knowledge that would then help me establish myself as a consummate professional in the future.*

**Academic Credentials**

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| **B. Tech** | Amrita School of Engineering | CGPA 7.73 | 2012 |
| **XII CBSE** | CMR National Public School, Bangalore | 82% | 2008 |
| **X CBSE** | CMR National Public School, Bangalore | 85% | 2006 |

**Skill set**

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| --- | --- | --- |
| **Languages** | : | C,C++,Verilog HDL |
| **Technical Tools**  **Special skill** | :  : | MATLAB, DSCH logic editor and simulator, WinAVR programmer, MASM 8086 Assembler, Autocad, Modelsim  Wavelet based signal processing, Speech processing, Multirate Signal processing and Image processing. |

**Projects**

**Final Year Project:**

**2D DCT Using CORDIC:**

Developed and tested an application to perform 2D Discrete Cosine Transform (DCT) on an input image implementing CORDIC algorithm, using Verilog. The results were verified using ModelSim and the code dump was created using Xilinx.

* The application can be implemented on an FPGA to perform DSP specific applications.

**Electives:**

1. **Wavelet based Signal Processing:**
   1. Design of Daubechies, Coiflet and Symlet wavelets using MATLAB.
   2. Analysis of signals using FFT, CWT and DWT in MATLAB.
   3. Major project on pitch estimation to determine sex and age of the speaker in a sample signal using Wavelets.
2. **Speech processing:**

* Created an application using MATLAB to detect voiced, unvoiced and silence regions in a given sample of speech.

1. **Multirate Signal Processing:**

* Created an application to perform up-sampling of a given sinusoidal wave using Hamming window for implementing a FIR filter.

**Hardware Design:**

* Implemented a DC Motor Speed Controller with PWM using only analog components, without any microprocessor.
* Made a digital clock using 8051 Microcontroller interfaced with an LCD.

**FPGA Project:**

* Used a Spartan 3 Xilinx FPGA kit to make a stop watch with the basic start-stop and reset functions.

**Work experience**

* Trained in Unix,Linux administration and Solaris administration
* Obtained ITIL v3 Certification
* Project experience in application management tool AppDynamics
* Also gained experience of working odd hours such as the night shift
* Learnt to adapt to work with different people during different shifts

**Strengths**

* + Cogent written and spoken communication skills.
  + Practical approach to problem solving.
  + Can work equally well in a group and as an individual.
  + Inquisitive and always willing to learn.
  + Can develop new skills in a short period of time.
  + Well versed with Office Suite

**Professional body membership**

IEEE Member

**Workshops**

Workshop on Swarm Robotics at National Institute of Technology, Suratkal, India

**Personal Dossier**

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| --- | --- | --- |
| **Date of Birth** | : | **28-07-90** |
| **Father's Name** | : | Venu Kumar Kesavan |
| **Languages known**  **Hobbies** | :  : | **English, Hindi, Malayalam, Kannada**  **Playing basketball, Reading novels, Listening to music** |
| **Passport Number** | : | **J8286320** |
| **Place of Issue** | : | **Bangalore** |
| **Address** | : | #103, Ramya Regency,  Jeevan Bima Nagar,  Bangalore 560075  Karnataka  INDIA |